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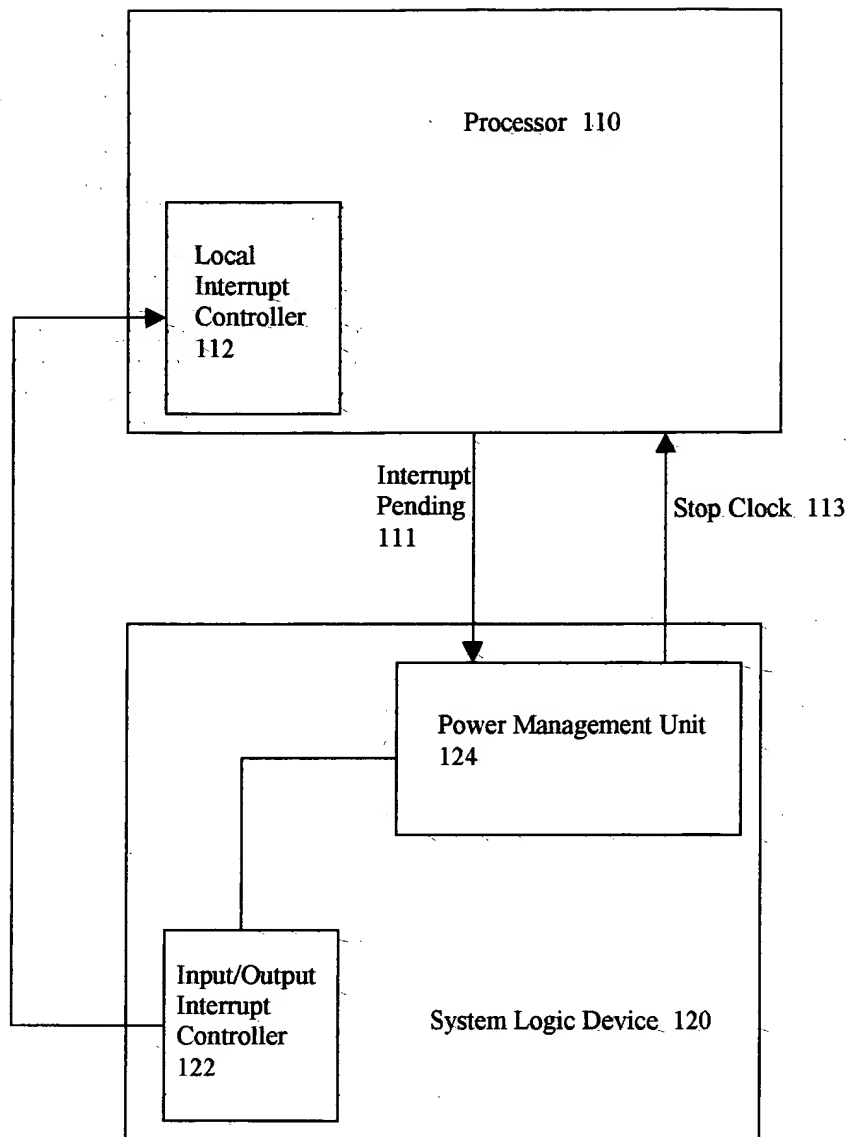


Figure 1

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graph TD
    A[Assert an edge-triggered interrupt to a processor 210] --> B[Deliver an interrupt pending signal from the processor to a power management device 220]
    B --> C{Processor in low power state? 230}
    C -- No --> D[No action required by power management device 240]
    C --> E[power management device causes processor to enter high power state to allow processor to service pending interrupt 250]

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Figure 2